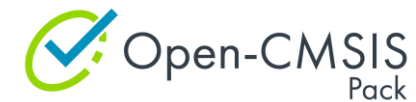


Open-CMSIS-Pack

Technical Project Meeting 2022-01-25

This meeting is recorded !



Agenda

- Welcome
- IDE Development Flow (outline)
- Project Manager Feedback
- Targeted Reference Example
- Wrap Up

IDE Development Flow with Open-CMSIS-Pack tools

1. Install the required packs using `cpackget` to work with devices/boards/software components
2. IDE dialogs create `*.yml` files (when a project/solution is started)
3. IDE dialogs support selecting devices, boards, components and management of files/groups.
4. IDE drop-down selections allow to choose the target-type and build-type that a user wants to work with.

This will get first prototyped with Keil Studio Cloud – the open-source version will be derived from it.

- In first VS Code deployment, `*.yml` file editing is supported by schema support in the editor.
5. A change to `*.yml` files should trigger **projmgr** that generates the output described including the `*.cprj` files used to build the project. The error message synch back to the related file.
 6. On “Build” the `*.cprj` files are used by CBuild to generate CMake files and run the build process.

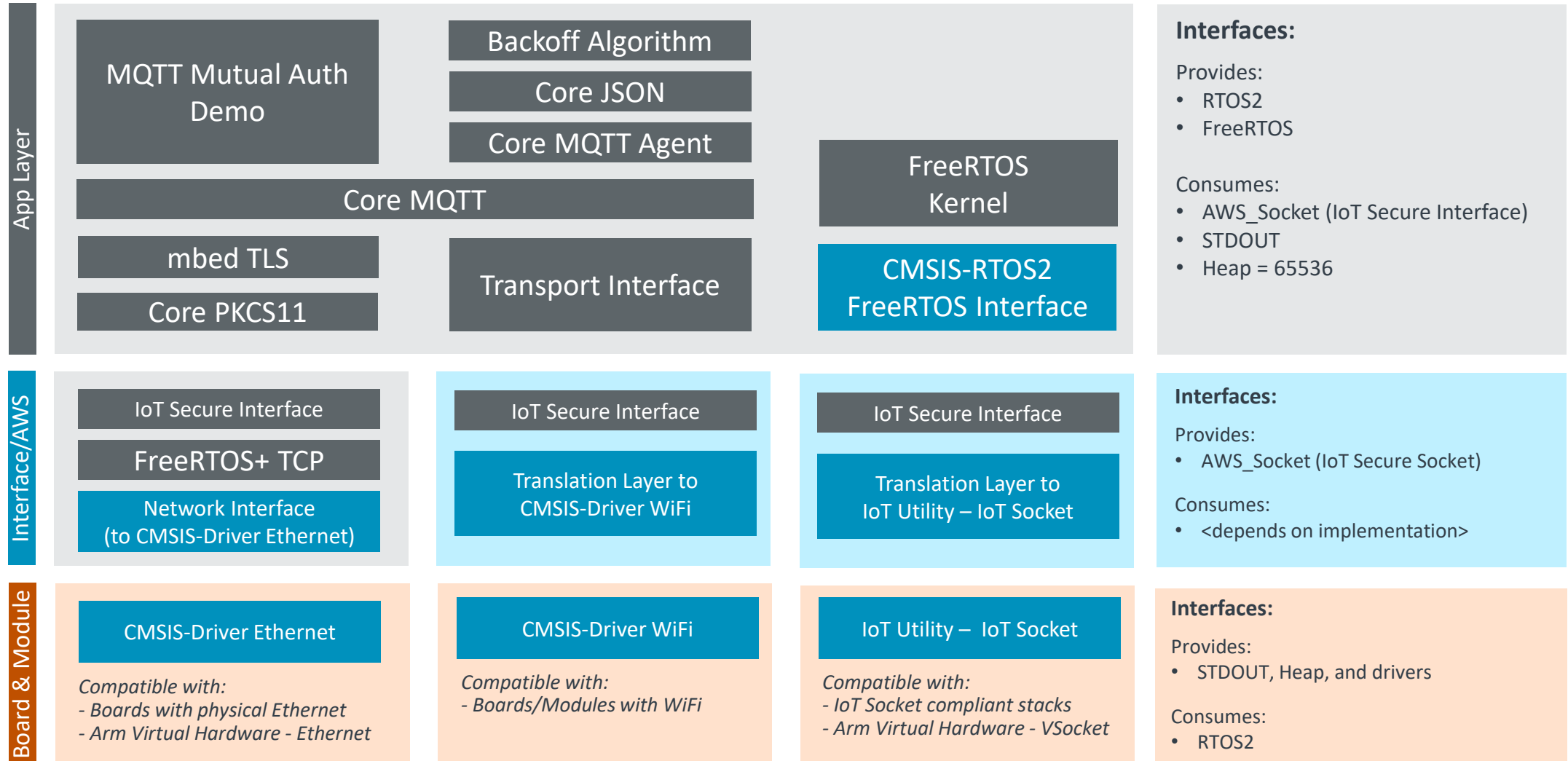
Suggest: we should get this working with VS Code to get a better feeling of the overall workflow.

CMSIS Project Manager – Proposal Review Feedback

Document [Overview.md](#) :

- <https://github.com/Open-CMSIS-Pack/devtools/pull/77> - got a lot of feedback
- Prioritization of next development focus – suggest to handle each in separate issues.
 1. PLM of config files (proposal is included in specification)
 - Specify a preferred 3-way merge utility?
 2. RTE directory structure finalize
 3. Local copy of components (ST requirement)
 4. Handling of packs (CMSIS-Pack-Root, Repositories, Local packs, etc.)
 - Finalize definition cdefault.yml
 5. CMSIS-Zone integration (resource management)
 6. Execution Groups (or perhaps better execution phases)
 7. Generator support / extensions
 8. Layer interface definitions
 9. Board conditions
 10. Layers distributed in packs
 11. Vendor specific additions (ST requirement) [devtools #83](#)
 12. Multiple devices defined by a board

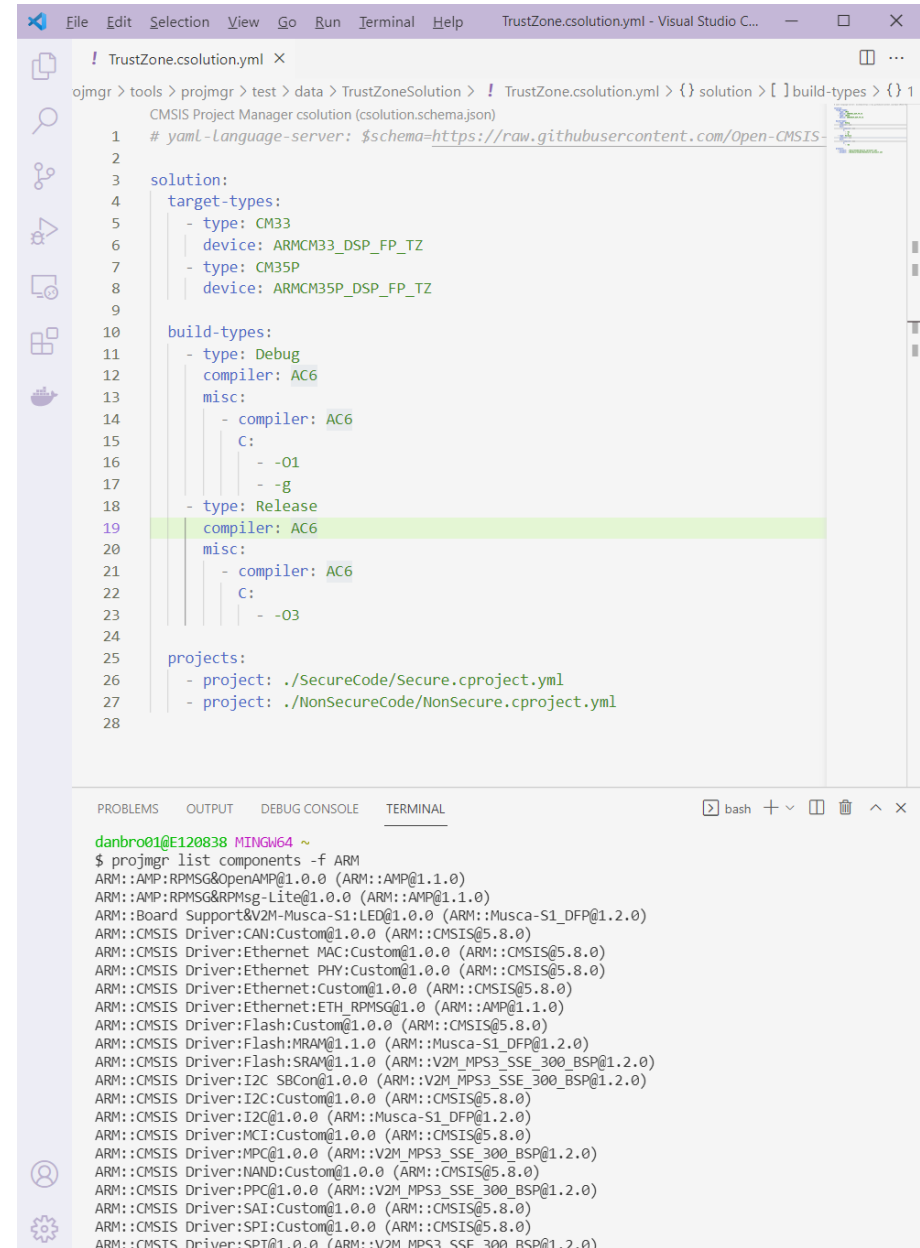
Structure of AWS Software Stack – example MQTT Auth Demo



This stack should be aligned with PSA to provide OTA, Key Storage, ...

Plan for next week

- Provide a development snapshot:
open-cmsis-pack-tools_0.9.0.zip
 - cpackget
 - projmgr + schemas
 - cbuilder



```
File Edit Selection View Go Run Terminal Help TrustZone.csolution.yml - Visual Studio C...
! TrustZone.csolution.yml X
ojmgr > tools > projmgr > test > data > TrustZoneSolution > ! TrustZone.csolution.yml > {} solution > [ ] build-types > {} 1
CMSIS Project Manager csolution (csolution.schema.json)
# yaml-language-server: $schema=https://raw.githubusercontent.com/Open-CMSIS-
1
2
3 solution:
4   target-types:
5     - type: CM33
6       device: ARMC33_DSP_FP_TZ
7     - type: CM35P
8       device: ARMC35P_DSP_FP_TZ
9
10  build-types:
11    - type: Debug
12      compiler: AC6
13      misc:
14        - compiler: AC6
15          C:
16            - -O1
17            - -g
18    - type: Release
19      compiler: AC6
20      misc:
21        - compiler: AC6
22          C:
23            - -O3
24
25  projects:
26    - project: ./SecureCode/Secure.cproject.yml
27    - project: ./NonSecureCode/NonSecure.cproject.yml
28

PROBLEMS OUTPUT DEBUG CONSOLE TERMINAL
$ projmgr list components -f ARM
ARM::AMP:RPMSG&OpenAMP@1.0.0 (ARM::AMP@1.1.0)
ARM::AMP:RPMSG&RPMSG-Lite@1.0.0 (ARM::AMP@1.1.0)
ARM::Board Support&V2M-Musca-S1:LED@1.0.0 (ARM::Musca-S1_DFP@1.2.0)
ARM::CMSIS Driver:CAN:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:Ethernet MAC:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:Ethernet PHY:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:Ethernet:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:Ethernet:ETH_RPMSG@1.0 (ARM::AMP@1.1.0)
ARM::CMSIS Driver:Flash:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:Flash:MRAM@1.1.0 (ARM::Musca-S1_DFP@1.2.0)
ARM::CMSIS Driver:Flash:SRAM@1.1.0 (ARM::V2M_MPS3_SSE_300_BSP@1.2.0)
ARM::CMSIS Driver:I2C SBCon@1.0.0 (ARM::V2M_MPS3_SSE_300_BSP@1.2.0)
ARM::CMSIS Driver:I2C:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:I2C@1.0.0 (ARM::Musca-S1_DFP@1.2.0)
ARM::CMSIS Driver:MCI:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:MPC@1.0.0 (ARM::V2M_MPS3_SSE_300_BSP@1.2.0)
ARM::CMSIS Driver:NAND:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:PPC@1.0.0 (ARM::V2M_MPS3_SSE_300_BSP@1.2.0)
ARM::CMSIS Driver:SAI:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:SPI:Custom@1.0.0 (ARM::CMSIS@5.8.0)
ARM::CMSIS Driver:SPI@1.0.0 (ARM::V2M_MPS3_SSE_300_BSP@1.2.0)
```

Wrap Up

- Actions all:
 - Provide review feedback
 - Suggest topics for next week's agenda
- Review of currently open issues in respository:
 - [Issues](#): Still relevant? Open Actions? Next Steps? Refinements?
- Next Open-CMSIS-Pack meeting: 1st Feb. 2022 @ 16:00 CET (15:00 UK)

Thank you

