





# Agenda

- Welcome
- Pack Specification Change Board
- Proposal on csolution "lockfile" #119 (Samuel)
- Proposal "permissive project list in csolution.yml" #376 (Samuel)
- Presentation: "Describing Parts in Open-CMSIS-Pack" (Silvio)
- Requests for Review
- Wrap Up



### Actions from last week

- ST/NXP to progress "Rediscuss the "hidden" proposal from NXP #112 (Fred)
- Provide example for using cdefault.yml file here: #356 (done)



### Review Pack Specification Change Board

#### **Decision Committee Meetings:**

- Meeting Notes (2022-07-12)
  - Accepted: #25, #124, #126, #131, #141
    - Action: create PR for schema and documentation (volunteers?)
  - Postponed #95 and #134 to next committee meeting
    - Clarify outstanding questions
- Next Decision Committee Meeting scheduled for 2022-08-29



## Topics presented by STMicroelectronics

- [csolution/cbuild] Using existing .cprj as lockfile #119
- [csolution] Permissive project list for csolution.yml #376
- [specification] Describing Parts in Open-CMSIS-Pack



## Requests for review

- Discuss Version 1.0.0 of csolution/cbuildgen
  - Agree requirements and "must fix" defects
- [cpackget] command download all public pdsc files #107
- [csolution] Proposal: misc C\_CPP instead of C\* #394
- [spec] Add <csolution> element to \*.PDSC format #134
  - The scope and implementation of the proposal is not clear enough yet.
- [csolution, cbuild] Optional RTE creation #371
- [csolution] compiler specific files and definitions #356
- [csolution] build-types are shared across all projects #355



# Wrap Up

• Issue #395 opened to vote for features and defects against csolution and chuild to be addressed before releasing a 1.0.0 version.

- Summer Vacation pausing weekly technical project meeting:
  - No meeting on Tuesday:
    - **26.07.2022**
    - **02.08.2022**
    - **09.08.2022**
    - **1**6.08.2022
- Next Open-CMSIS-Pack meeting: 19th July 2022 @ 16:00 CET (15:00 UK)





