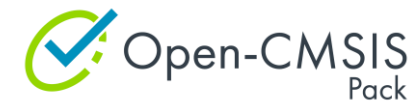# Open-CMSIS-Pack

Technical Project Meeting 2023-09-19

This meeting is recorded !

Agenda

- Welcome

- Project Boards

- Machine Learning DevOps

- Taxonomy

- Issues to Review

- Wrap Up

Boards:

- [Open-CMSIS-Pack Specification Change Board](#)

- [CMSIS-Toolbox 2.2 Project Board](#)

  - See progress and issues in scope for version 2.2.0

  - Please review and provide feedback in case you see topics missing

  - Add issues or comment on existing issues that you think should be added to 2.2.0

- [CMSIS-Toolbox 2.3 Project Board](#)

Machine Learning DevOps

- https://github.com/arm-software/AVH-MLOps
- Docker Container
- GitHub Actions
- Examples
  - https://github.com/ARM-software/AVH-MLOps/tree/main/mlek-kws
  - Build
  - Test
  - Analyze

Taxonomy (feedback)

- Protege as tool for taxonomy modeling - feedback [#251](#251)

- [Open-CMSIS-Pack-Taxonomy](#) repository - feedback [#252](#252)

- Help finding good rules for different purposes:
  - Cclass / Cgroup defining API + Implementation
  - Cclass as a collection of different purposes: "Utility" -
  - Cclass / Cgroup specifying a group of components belonging to a product

Taxonomy case Memfault

- Memfault::Utility&Memfault

  - Memfault::Utility&Memfault:Memfault Core

  - Memfault::Utility&Memfault:Memfault Coredump RAM Backend

  - Memfault::Utility&Memfault:Memfault Platform implementation

Utility		Memfault	⌄			Generic software utility components
Memfault Core	☐			Memfault	1.2.5	Memfault core components
Memfault Coredump RAM Backe...	☐			Memfault	1.2.5	RAM backed coredump implementation
Memfault Platform Port	☐			Memfault	1.2.5	Template files for Memfault Platform implementation

- Using Cgroup

  - Memfault::Utility:Memfault Debug:Core

  - Memfault::Utility:Memfault Debug:Coredump RAM Backend

  - Memfault::Utility:Memfault Debug:Platform implementation

Utility			⌄			Generic software utility components
Dummy Util	☐			MyVendor	0.0.1	Dummy Utility
Memfault Debug						
Core	☐			Memfault	1.2.5	Memfault core components
Coredump RAM Backend	☐			Memfault	1.2.5	RAM backed coredump implementation
Platform Port	☐			Memfault	1.2.5	Template files for Memfault Platform implementation

Issues to Review

- [csolution] gap: Support for multiple component instances [#1123](#)
- [RTE] API version selection/resolution may be incorrect (bug) [#1124](#)
- [csolution] *.cbuild-idx.yml always lists all contexts of a solution [#1125](#)
- [csolution] *.cbuild-set.yml lists all selected contexts from last invocation [#1127](#)
- Implement Simplified Generator Proposal [#1112](#) - in progress
- Create *.cbuild-gen.yml with absolute paths [#975](#) - *pending* - Erik Mallberg
- Policy on breaking changes in PACK schema - Discussion [#253](#) - *no comments*
- [cmsis-toolbox] Windows: long pathname support [#838](#) - *trial attached binaries*
- [csolution] Add `warnings:` option `all` in yml input [#974](#) - *no comments* - Tarek
- Add Memory Information to build information files [#1117](#) - *no comments*
- Implement Open-CMSIS-Pack lock file [#1122](#) (STMicroelectronics) - *no comments*
- [CMakeLists Proposal](#) - leave comments and feedback in [#1044](#) - *no comments*

Wrap Up

Is anyone preparing/working on a topic to present and discuss in the coming weeks?

- Please contact Joachim.Krech@arm.com ahead of the meeting

!!! No meeting on Tuesday 2023-10-03 (public holiday in Germany) !!!

Next Open-CMSIS-Pack meeting: 26th Sep 2023 @ 16:00 CET (15:00 UK)

Thank you